CLAIM LISTING

Please amend the claims as follows:

 (Currently Amended) A method for deterministic registration for communication networks comprising:

transmitting a node register command over a network, the node register command comprising a plurality of bits, the plurality of bits addressing a range of potential nodes;

determining, by a particular node and based upon the plurality of bits, whether the particular node corresponds to the range of potential nodes;

determining, by the particular node and based upon the plurality of bits and an identifier of the particular node, a corresponding time delay;

listening to the network for a response from a node in the range of nodes;

determining, based upon the time slot delay in which the response is received, the particular node in the range of nodes from which the response was received; and

responsive to detecting the response, registering the node;

during a first portion of the time slot delay, determining a level of ambient noise in a network:

determining a ceiling of the level of ambient noise;

setting a threshold for a good signal to a predetermined level above the ceiling of the level of ambient noise; and

during a second portion of the time slot delay, listening to the network for a signal,

- (Previously Presented) The method of claim 29, wherein the second plurality of bits are padded with zeros.
- (Previously Presented) The method of claim 29, wherein the node register command further comprises a third plurality of bits.
- 4. (Previously Presented) The method of claim 1 further comprising: creating a confirmation packet; transmitting the confirmation packet; and during the corresponding time slot delay, transmitting a signal based on a registration status of the particular node, the signal being a confirmation of the registration of the particular node.
- (Previously Presented) The method of claim 1 further comprising, the time slot delay, calibrating a receiver during a first portion of the time slot delay.
- (Previously Presented) The method of claim 1, wherein the time slot delay is a
 response period during which at most one node may transmit a message in response to the node
 register command.
- (Previously Presented) The method of claim 4 further comprising, during the time slot delay, not transmitting a signal if the corresponding node is not registered.
 - 8.-28. (Cancelled)

| 29. | (Currently Amended) The method of claim 1, where [[]] in the plurality of bit | ts |
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| comprises a f | rst plurality of bits and a second plurality of bits. | |

- 30. (Cancelled)
- 31. (Cancelled).